

Serial No. 09/595,039
Attorney Docket No. E0902
Firm Reference No. AMDSP0379US

Reply to Office Action Dated January 28, 2004
Reply Dated February 25, 2004

AMENDMENTS IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A network medium interface card for coupling a device to a network medium, the interface card comprising:
first and second blocks,
an external interface for coupling to the network medium, and
a switchable connection,
wherein the switchable connection may be selectively configured either to internally connect the blocks to each other, or to connect one of the blocks to a transmit portion and/or a receive portion of the external interface.
2. (Currently Amended) The device card of claim 1, wherein the external interface is a media independent interface (MII).
3. (Currently Amended) The device card of claim 2, wherein the MII transmits and receives data in four-bit wide data stream.
4. (Currently Amended) The device card of claim 1, wherein one of the blocks includes a physical layer device (PHY).
5. (Currently Amended) The device card of claim 1, wherein one of the blocks includes a media access controller (MAC).
6. (Currently Amended) The device card of claim 1, wherein the switchable connection is a first switchable connection, and further comprising a third block and a second switchable connection which may be selectively configured either to internally connect the third block to the

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first block, or to connect either the first ~~sub~~ block and/or the third ~~sub~~ block to the transmit portion and/or the receive portion of the external interface.

7. (Currently Amended) The ~~device~~ card of claim 6, wherein the first block includes a media access controller (MAC).

8. (Currently Amended) The ~~device~~ card of claim 6, wherein the third block includes a bus interface.

9. (Currently Amended) The ~~device~~ card of claim 8, wherein the bus interface is a peripheral component interconnect (PCI) interface.

10. (Currently Amended) The ~~device~~ card of claim 6, wherein the second block includes a physical layer device (PHY).

11. (Currently Amended) The ~~device~~ card of claim 6, wherein the first and second switches may be configured to test operation of the first block by connecting receive and transmit ends of the first block to respective portions of the external interface.

12. (Currently Amended) A method of testing operation of an internal block of a network medium interface device, comprising:

reconfiguring the device so that a normally-internally-connected connection of the block are is connected to an external interface;

inputting test signals to the block; and

evaluating output of the block.

13. (Original) The method of claim 12, wherein the reconfiguring includes connecting two normally-internally-connected connections of the block to the external interface.

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14. (Original) The method of claim 12, wherein the reconfiguring includes configuring one or more switchable connections operatively coupled to the block.

15. (Original) The method of claim 14, wherein the configuring includes sending signals to the one or more switchable connections.

16. (Original) The method of claim 15, wherein the sending signals includes sending the signals through pins of the network medium interface.

17. (Original) The method of claim 12, wherein the external interface is a media independent interface (MII).

18. (Original) The method of claim 12, wherein the block includes a media access controller (MAC).

19. (Original) The method of claim 12, wherein the block includes a physical layer device (PHY).

20. (Original) The method of claim 12, wherein the block includes a second external interface.

21. (Original) The method of claim 20, wherein the second external interface is a bus connector.

22. (New) A network medium interface device comprising;
a media access controller (MAC);
a physical layer device (PHY);
an external interface; and
a switchable connection;

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wherein the switchable connection may be selectively configured either to internally connect the MAC to the PHY, or to connect either the MAC or the PHY to a transmit portion and/or a receive portion of the external interface.

23. (New) The device of claim 22, wherein the switchable connection may be configured to test operation of the MAC and or the PHY by connecting respective receive and transmit ends of to respective portions of the external interface.

24. (New) The device of claim 22, wherein the switchable connection is a first switchable connection, and further comprising a third block and a second switchable connection which may selectively configured either to internally connect the third block to the MAC, or to connect either the MAC and/or the third block to the transmit portion and/or the receive portion of the external interface.

25. (New) The device of claim 24, wherein the third block includes at least one of an additional MAC, an additional PHY and a bus interface.

26. (New) The device of claim 25, wherein the first and second switches may be configured to test operation of the additional MAC by connecting receive and transmit ends of the additional MAC to respective portions of the external interface.

27. (New) The device of claim 25, wherein the first and second switches may be configured to test operation of the additional PHY by connecting receive and transmit ends of the additional PHY to respective portions of the external interface.

28. (New) The device of claim 25, wherein the first and second switches may be configured to test operation of the third block by connecting receive and transmit ends of the third block to respective portions of the external interface.